



#75-4-04
Dohmen 9-1-4-9

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicants(s): Dohmen et al.
Case: 9-1-4-9
Serial No.: 09/976,729
Filing Date: October 12, 2001
Group: 2133
Examiner: Joseph D. Torres

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: *John M. Mauri* Date: April 14, 2004

Title: High Speed Syndrome-Based FEC Encoder and Decoder and System Using Same

INFORMATION DISCLOSURE STATEMENT

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Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following document listed on the accompanying PTO Form 1449. A copy of the listed item is enclosed.

1. Mastrovito, "VLSI Designs for Multiplication Over Finite Fields $GF(2^m)$," Int'l Conf. On Applied Algebra, Algebraic Algorithms, and Error-Correcting Codes, pgs. 297-309, Rome (July 1988).

Please charge the amount of \$180.00 in accordance with 37 C.F.R. §1.17(p) to **Deposit Account No. 50-0762**. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or the credit **Deposit Account No. 50-0762** as required to correct the error.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability or that no other material information exists.

Respectfully submitted,

Date: April 14, 2004

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